

## AN 409 Detection of Threading Dislocations in Strained Si Using AFM

May 7, 2007 (Version 3.0)

## **Discussion**

Strained Si is being evaluated as a channel material in CMOS devices for its higher carrier mobility. The manufacturing of the strained Si layer is achieved by epitaxially depositing a thin (5 to 50 nm) silicon layer on top of a strain-relaxed Si<sub>1-x</sub>Ge<sub>x</sub> buffer layer. Usually, threading dislocations are present on the surfaces of the strain-relaxed Si<sub>1-x</sub>Ge<sub>x</sub> buffer layers and propagate through the epitaxially grown thin Si layer (strained Si) to the surface.

Because the presence of threading dislocations can seriously degrade device performance, minimizing the density of threading dislocations has been one of the key manufacturing challenges of strained Si. Etch Pit Density (EPD) and Plan-view Transmission Electron Microscopy (PVTEM) are the most common metrology techniques for monitoring the threading dislocation density. However, both techniques are destructive because the wafer is destroyed due to sample preparation steps. Furthermore, the etching solution used in the EPD technique is hazardous and the PVTEM is limited by the field of view. In comparison, Atomic Force Microscopy (AFM) is a better metrology technique for monitoring the threading dislocation density directly on the whole 8" or 12" wafer at atmospheric conditions without any sample preparation steps (i.e. AFM is non-destructive and does not require hazardous etching solutions). In addition, AFM can operate at a larger field of view than PVTEM. The AFM detection limit for threading dislocation density is approximately 2.5 X 105cm<sup>-2</sup> for a 20um X 20um scan size and 4 X 104cm<sup>-2</sup> for a 50um X 50um scan size.

Samples A and B are two examples of strained Si grown on a strain-relaxed Si<sub>1-x</sub>Ge<sub>x</sub> layer, which has been planarized using chemical mechanical polishing (CMP). Figures 1(a-c) show three different sizes of AFM images from the strained Si surface of sample A. These images were acquired at the location about 1cm from the wafer edge. Many threading dislocations are observed in Figure 1(a) and also shown in the 20um X 20um zoom-in view of Figure 1(b). One of the threading dislocations is imaged with a higher magnification of the 5um X 5um image size and presented in the 3-D perspective view as shown in Figure 1(c). The observation of higher threading dislocation density at this location is consistent with previous reports that the threading dislocation density is higher near the wafer edge.

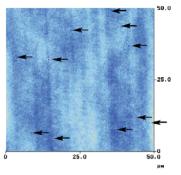


Figure 1(a): AFM image of strained Si Surface from sample A. Arrows indicate numerous threading dislocations that were detected near the wafer's edge.

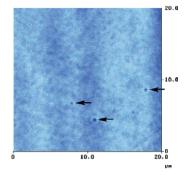


Figure 1(b): Threading dislocations zoom-in from Figure 1(a).

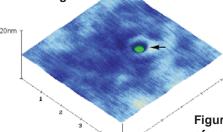


Figure 1(c): 3-D perspective view of one threading dislocation zoom-in from Figure (b).



Figures 2(a-c) also show three AFM images acquired from the strained Si surface of the other CMP'ed sample B. These images were acquired at a location away from the wafer edge. There are six threading dislocations observed in Figure 2(a) and three of them are zoom-in viewed in the 20um X 20um image of Figure 2(b). Figure 2(c) is the 3-D perspective view of one threading dislocation within a 5um X 5um area selected from Figure 2(b). This latter figure also shows that sample B has a significantly rougher surface surrounding the dislocation compared to surface surrounding the dislocation on sample A (Figure 1(c)).

In this brief note, AFM is shown to be a viable technique for detecting and monitoring the threading dislocations in strained Si.

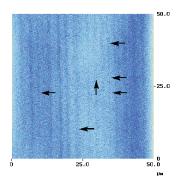


Figure 2(a): AFM image of strained Si Surface from sample B. Arrows indicate six threading dislocations that were detected away from the wafer's edge.

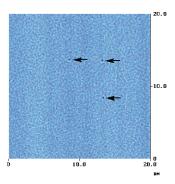


Figure 2(b): Three threading dislocations zoom-in from Figure 2(a).

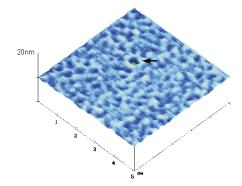


Figure 2(c): 3-D perspective view of one threading dislocation zoom-in from Figure 2(b).

## **United States Locations**

Tempe Arizona

- +1 480 239 0602 info.az@eaglabs.com
- +1 602 470 2655 fax

Sunnyvale, California 810 Kifer Road

- +1 408 530 3500 info.ca@eaglabs.com
- +1 408 530 3501 fax

1135 E Arques Avenue

- +1 408 738 3033
- +1 408 530 3035 fax

785 Lucerne Drive

- +1 408 737 3892 +1 408 737 3916 fax

Peabody Massachusetts

- +1 978 278 9500 info.ma@eaglabs.com
- +1 978 278 9501 fax

Chanhassen, Minnesota

- +1 952 828 6411 info.mn@eaglabs.com
- +1 952 828 6449 fax

East Windsor, New Jersey

- +1 609 371 4800 info.nj@eaglabs.com
- +1 609 371 5666 fax

Syracuse, New York

- +1 315 431 9900 info.ny@eaglabs.com
- +1 315 431 9800 fax

Raleigh, North Carolina

- +1 919 829 7041 info.nc@eaglabs.com
- +1 919 829 5518 fax

Round Rock, Texas

- +1 512 671 9500 info.tx@eaglabs.com
- +1 512 671 9501 fax

## International Locations

Shanghai China

- + 86 21 6879 6088 info.cn@eaglabs.com
- + 86 21 6879 9086 fax

Tournefeuille, France

- + 33 5 61 73 15 29 info.fr@eaglabs.com
- + 33 5 61 73 15 67 fax

Frankfurt, Germany

- + 49 (0) 693053213 info.de@eaglabs.com
- + 49 (0) 69307941 fax

Tokyo, Japan

- + 81 3 5396 0531 info.jp@eaglabs.com
- + 81 3 5396 1930 fax

HsinChu, Taiwan

- + 886 3 5632303 info.tw@eaglabs.com
- + 886 3 5632306 fax

Uxbridge, United Kingdom

- + 44 (0) 1895 811194 info.uk@eaglabs.com
- + 44 (0) 1895 810350 fax